EFFECT OF MOTHFLAME OPTIMISATION ON TOTALHARMONICDISTOR-TIONFORSELECTIVE HARMONIC ELIMINATION

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Abstract

Multilevel Inverter (MLI) has lately emerged as avery significant option in the areas of energy regulation with high-intensity medium voltage. It consists of number of advantages; however, despite of its benefits, its performance is majorly affected due to the presence of harmonics. Numerous approaches have been presented in literature to reduce the harmonic distortion in MLIs. However, it has been analyzed that they did not yield effective results as they consists of certain limitations. Therefore, to overcome the previous limitations and to efficiently reduce the harmonics from MLIs, an ovelap proach is proposed in this work. In the proposed work, moth flame optimization (MFO) approach is implemented. The main purpose of designing the novel approach is to remove the harmonic distortion and selective harmonic of 5th, 7th and 11th order in MLIs and achieve better results. This proposed approach is implemented in the MATLAB environment to analyze its performance. Also, the comparative analysis is performed between proposed approach and previous approaches in terms of THD. And all the obtained results of simulation demonstrate the efficacy of proposed approach over convention alones.

Index Terms – Multilevel Inverters (MLIs), Selective HarmonicElimination(SHE), TotalHarmonicDistortion(THD), MothFlameOptimization (MFO)

1.INTRODUCTION

MultilevelInverter(MLI)haslatelyemergedasaverysignificant option in the areas of energy regulation withhigh-intensity medium voltage. Staircase waveform can begenerated by synthesizing the terminal voltage of the ACoutput from several DC voltages. MLIs have been a realisticand effective method for eliminating losses from switches. The synthesized output provides new steps on the DC sideas the number of voltage levels increases, producing theresulthatchallengesthesinusoidalwavewiththesmallestnumberofharmonic-distortion. The general function of MLI is to establish a sinusoidal voltage frommanyvoltageratescharacteristically derived from condenser voltages ources [1].

MLIs are calculated for a growing range of applications due to their large power efficiency combined with smallers witching losses and harmonics of lower performance. MLIs have been approficient and realistic approach for rising strength and reducing harmonics of ACloads [2]. Most desirable characteristics of MLIs are as:

- MLI can generate exceptionally low dv / dt outputvoltagesanddistortion.
- MLI,drawfeedbackcurrent,withverylittledistortion.
- MLIgeneratessmallercommon-mode(CM)voltage,thus reducing the stress of the motor bearings. In addition, CM voltages may be removed using advanced modulation techniques.
- MLI can operate at a lower frequency switch-

ingtechnique.

The stepwise output voltage is the key benefit of MLIs asopposedtoconventionaltwo-levelvoltage-sourceinverters. This results in higher power efficiency, lowers witching losses, improved electromagnetic-stability, higher voltage ability, and a transformer's pointlessness at the stage of distribution voltage, thereby reducing the expense.

The MLI has been deployed in numerous applications-varying from medium to high-power stages, such as power conditioning systems, motor drives, as well as traditional and modern generating capable electricity [3]. The different MLI structures are [4]:

- 1. Diode-clamped(neutral-clamped)
- 2. CascadedH-bridgeinverter
- 3. Capacitor-clamped(flyingcapacitors).

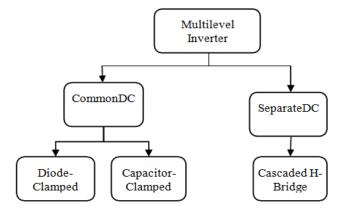


Figure 1: MLITopologies

MLI topologies are different ways to reduce the harmonic content which keeps the number of switches small. In essence, Harmonics are unwanted higher frequencies that superimposed a distorted wavepatter non the fundamental waveform. They can produce electric-waveforms with greater voltage levels, at the cost of higher switching and control algorithm complexity. The harmonic efficiency is further enhanced where the preprogrammed switching schemes defined as the SHE and Selective Harmonic Mitigation (SHM) are used with MLI.

SELECTIVEHARMONICELIMINATION(SHE):

SHE is a method for eliminating certain chosen harmonics from the square wave by adding extrasquarewaveswitches. Back in 1973, it was Hasmukh and Richard whodeveloped the SHE methodology [5]. In SHE, the output isturned on and off many times over each half cycle of thesquare wave, and these changes are spread equally overeachhalfcyclewithquarter-wavesymmetry. Theimmediate transition of each switching is measured fromthe y-axis is called the switching angles but every set ofswitching angles is linked through Fourier who developed the scendental trigonometric equations mathematically to reflect the harmonic content of a squarewave. WhenimplementingtheSHEPWM(pulse-widthmodulation)byproperlyswitchingonandoffatthepreferred moment, some odd harmonics, usually a set oflower order harmonics, can be selectively eliminated from the inverter's output voltage and the magnitude of thefundamentalvoltageisregulated[6]. The higher-orderharmonicscanbequicklyextractedthroughlimitedphysicalscalepassivefiltersandatarelateddecreasedexpense.

There are different solving algorithms and techniques for SHE such as numerical approaches, optimization-based approaches and other approaches. Some of the techniques are reviewed innext section:

II. LITERATUREREVIEW

Numerousapproacheshavebeenproposedinordertoremove the harmonic distortion or selective harmonics inthe MLIs. Some of the techniques presented in literaturearereviewedwhicharediscussedinthissection:

Improvement of harmonics can be viewed as an advancedway of reduction of harmonics. The work [7] applied aninnovative version of the process of particles warmoptimization (PSO) to 7-level inverters in this way.

The paper [8] proposed the latest approach for optimizingharmonicsteppedwaveformbyusingharmonyquestalgorithmforMLIs.

The work done in [9] addressed the removal of substantiallower order 5th harmonic in five-stage cascaded invertersusing sine cosine algorithm (SCA).

S. Bhalerao, et al., [10] had introduced the design and real-timeimplementationoftheBipolarSHE-PWMintheSingle-Phase 2-levelInverter for Lower Order HarmonicSuppressionscheme.

Theresearch[11]describedindepththeimplementationof the SHE-PWM technique to thirteen-level cascaded subMLI.

Thereduction of unnecessary harmonics in a MLI with equivalent DC sources was described in the paper [12] using bat evolutionary optimization method.

A. Kavousi, et al. [13] introduced the Beeoptimization approach in a case add MLI for harmonic elimination.

The study [14] explored the critical parameter modelingmethodology for single-phase systems on a cascaded H-Bridge (CHB) with SHE/SHC.

M. T. Yaqoob [15] provided the review on MLIsfor SHEthat PSO is stronger in terms of time and output than the Genetic Algorithm, Imperialist Competitive Algorithm, Bee Algorithm, and Differential Algorithm.

Application of PSO algorithm to find the optimum solution of the flipping angles in a 3-ph seven-level Cascaded MLIwith unequal DC sources for the removal of lower-orderharmonicswasdescribed in the paper [16].

In the article [17], the author had suggested a procedure-forsolving the optimization by using SCA algorithm to eliminate the selective harmonics in CHBMLI.

In [18], the optimization based technique had been used toeliminate the 5th, 7th and 11th order harmonics of nine-levelinverter. Forthis, authorhad presented SCA approach.

However, it has been analyzed that SCA comprised of someproblems that affect the system's performance. Therefore,the SHE problem cannot be eliminated completely with the SCA and a novel algorithm should be implemented with better performance.

III. PRESENTWORK

Toremoveharmonic distortion in MLIs, various optimizational gorithms based approaches are designed and some of these are reviewed in above section. One of these approaches proposed is SCA based approach which had been considered as effective approach. However, asmentioned above, it is an alysed that SCA comprises of various issues. These issues include slow convergence and falling into local solutions. Considering these demerits of SCA algorithm, there is a requirement of modifications in the existing model.

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Thus, a novel approach is designed in which moth flameoptimization (MFO) algorithm is implemented in the placeofSCA.MFOalgorithmisinspiredbythenavigationmethods of the moths. Moths are capable of maintainingthe fixed angle with the moon that is helpful in making therightanglefor switching pulsesof thediodes. As 9-level inverter is utilized in the existing technique, themain purpose of designing the novel approach is to remove the harmonic distortion and selective harmonic of 5th, 7thand 11th order in MLIs and achieve better results. MFO isresponsible for making the best decision about switchingthe pulses and efficient conversion from DC to AC. MFOseemstobetterintermsofconvergence-duetoitsnavigation method. It is also better than SCA-because itdoesnotfall intothe localsolutions.

Therefore, by proposing MFO based optimization technique, the problem of SHE can be eradicated.

The schematic diagram of the proposed system is shownbelow:

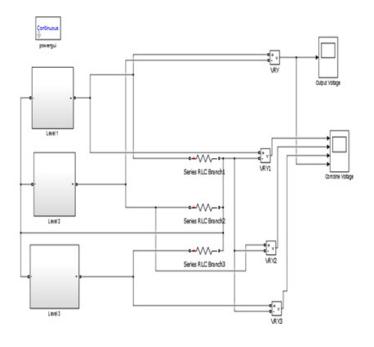


Figure2:Schematicdiagramofproposedsystem

The figure 2 shows the simulink model of the proposedsystem. In this, the three different levels i.e. level 1, level 2 and level 3 are designed. These three levels generate thethree-phaseoutputandreturnthevoltage. Thelevelhere

performance of this system is then analyzed further to verify that whether the generated output of the proposed system gives the efficient results in terms of harmonic distortion. The results of the performance analysis obtained are represented in the next section.

IV. RESULTS AND DISCUSSION

As stated in the above section, the new approach i.e., MFO is used in the proposed work to resolve the problem of SHE in MLIs. Now, this proposed approach is implemented in the MATLABenvironmentand simulation is performed in order to analyze its performance in terms of different parameters. And obtained results of the simulation are discussed in this section.

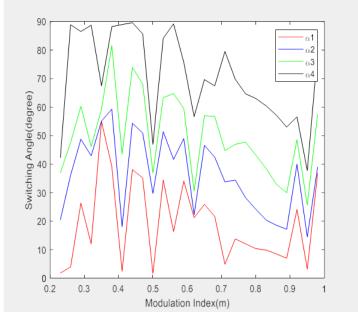


Figure 3: Switching angles in proposed approach withrespecttomodulationindex

The graphshown in figure 3 illustrates the calculated-switching angles i.e. $\alpha 1, \alpha 2, \alpha 3$ and $\alpha 4$ with respect to modulation index (m). The modulation index is defined mathematically as:

V*s

represents the inverteri.e. 9 level cascaded H-bridge (CHB) MLI. The pulses that are given to the inverter are generated by using the MFO optimization algorithm. These

$$M= f$$

Vdc (1)

pulses are generated on the basis of switching angle. Thesepulsesarethenfedtothe9-levelCHBMLIwhich-thengenerates the output voltage. The output voltage of all thethreelevelsarethencombinedlastlywhichgives-theoverallgeneratedoutputvoltageofthesystem. TheIn which, s represents number of dc sources,-Vdc represents supply voltage andVfimplies fundamentalvoltage.Inthegraph,they-axiscalibratesthevalue-ofswitching angles that ranges from 0 to 90 degree, and

x-axiscalibratesthevaluesofmodulationindexrangingfrom 0.2to1. Theswitching angledare calculated in such away that they can result in reduction of THD of the system.

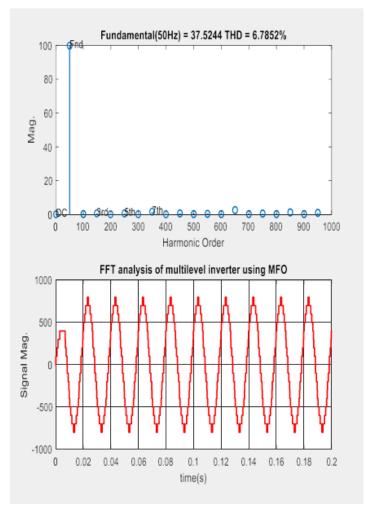


Figure4:FFTanalysisofMLI usingproposedMFO

The FFT analysis of the MLI is performed using proposedMFOapproachasshowninfigure4. The abo-The bargraphshowninfigure5depictsvegraphinthe theresultsofcomparative analysis performed between MFOapproachandconventionalNRand-SCAapproachesinterms of THD. On analyzing the graph, it is clearly depicted that among these three approachesNRisthemostinefficientapproachasithasthehighestvalueofTHD.Whereas, SCA is quite efficient than NR as its THD values are lower than NR. However, the THD value of proposedMFOapproachisevenlowerthanthatofSCA, which demonstrates the superiorityoftheproposedapproachover these two conventional approaches i.e. SCA and NR, with regardsto THD. figureshowsthemagnitudeof3rd,5thand7thorderharmonics and THD which is 6.7852%. In that graph, the y-axisandx-axiscalibratesthevaluesofmagnitudeandharmonicorder, respectively. In this, the range of magnitude varies

from 0 to 100 and that of harmonic ordervaries from 0 to 1000. And the below graph in the figureexemplifies the signal magnitude of FFT analysis of MLI. Inthis graph, the signal magnitude of the MLI using proposedapproach is shown with respect to varying time. The signalmagnitude varies between range -1000 and 1000, and thetime varies from 0sec to 0.2 sec. The results imply thatproposedapproachminimizesthetargetedharmonicsefficiently.

Now,thecomparative analysis is performed between proposed MFO approach and conventional approaches i.e.NR and SCA approach, in term of THD and the obtained results are represented below:

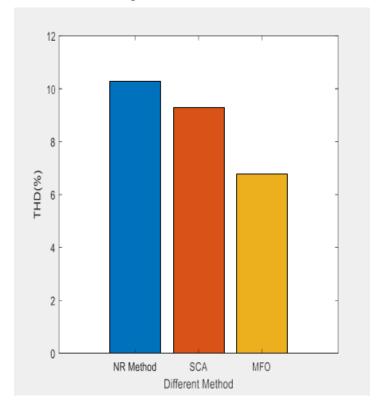


Figure 5: Comparative analysis in terms of THD

The values of two different parameters i.e. THD andimprovement, for proposed and conventional approaches, are recorded in table 1.

Sr.	Parameter	NR	SCA	MFO
No.				
1	THD(%)	10.27	9.28	6.78
2	Improve- ment(%)		9.63	26.9397

Table 1: THD and improvement values of differentapproaches

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The proposed and conventional approaches are compared terms of their THD and improvement, and the obtained values are recorded in above table. It is clearly demonstrated from the shown values that NR has the maximum value of THD i.e. 10.27%, followed by SCA i.e. 9.28%, whereas proposed MFO approach has the minimum value of THD i.e. 6.78., and thus it is the most efficient technique than other two previous approaches. Also, the NR does not shows any improvement, whereas, the value of improvement of SCA is 9.63% and that of proposed MFO is 26.9397%, which is highest than other approaches. Thus, this demonstrates the efficacy of the proposed approach.

V. CONCLUSIONANDFUTURESCOPE

The novel approach is designed in the proposed work toremove the harmonic distortion and selective harmonic of5th, 7th and 11th order in MLIs. In this proposed approach, the MFO is used to resolve the problem of-SHE. The simulation of this proposed approach is then carried out in he MATLAB environment to analyze its performance. Forsimulation, different parameters are takenintoconsiderationsuchasswitchingangle, THD and FF-Tanalysis. Also, the comparative analysis is performedbetween proposed approach i.e. MFO-based approach and conventional approaches i.e. NR and SCA in terms of THD.And obtained results revealed that proposed approach ismore efficient than the previous approaches in terms of THD as it has lowest THD i.e. 6.78 than theotherapproaches. Also, the proposed approach has the highestimprovementvaluei.e.26.9397thantheprevioustwoapproaches. Therefore, from all the results, it has beendemonstratedthatproposedapproachhashighefficacywith regardstoalltheconsidered parameters.

For future work, the fitness function of the system can beupdatedand systemperformance can be then analyzed. And also, the hybrid optimization technique can beusedinitforelimination of harmonic distortions from the MLIs.

REFERENCES

- [1] Rajaram, M"Certaininvestigationsonmeta-heuristical gorithms based selective harmonic elimination in multilevel inverters", http://hdl.handle.net/10603/182142,2016.
- [2] Lai, J. S. and Peng, F. Z. "Multilevel converters A newbreedofpowerconverters," IEEE Translations on Industrial Applications, Vol. 32, No. 3, pp. 509–517, 1996.
 [3] Rodriguez J., Franquelo L. G., Kouro S., Leon, J.I., Portillo R. C., Prats M. A. M., Perez M. A. "-

- MultilevelConverters: An Enabling Technology for High-PowerApplications", Proceedings of the IEEE, vol.97, no.11,pp.1786-1817,Nov.2009.
- [4] Anjali Krishna R, Dr L Padma Suresh, "A Brief ReviewonMultiLevelInverterTopologies",2016,InternationalConferenceonCircuit,PowerandComputing Technologies(ICCPCT)
- [5] HasmukhSPatelandRichardGHoft,"Generalizedtechniquesofharmoniceliminationandvoltagecontrolinthyristorinverters:PartI-harmonic
- elimination",IEEETransactiononIndustryApplications,vol, IA-9,no.3, pp.310-317,1973
- [6] M. Mythili and N. Kayalvizhi, "Harmonic minimizationinmultilevelinvertersusingselectiveharmoniceliminationPWMtechnique,"2013International-ConferenceonRenewableEnergyandSustainableEnergy(ICRESE),Coimbatore,2013,pp.70-74,doi:10.1109/ICRESE.2013.6927790.
- [7] K. Kaviani, S. H. Fathi, N. Farokhnia and A. J. Ardakani,"PSO, an effective tool for harmonics elimination and application in multi-level inverters," 2009 4th IEEEConference on Industrial Electronics and Applications, Xi'an, 2009, pp.2902-2907, doi:10.1109/IC-IEA.2009.5138740.
- [8] B.Majidi,H.R.Baghaee,G.B.Gharehpetian,J. MilimonfaredandM.Mirsalim,"Harmonicoptimization-inmulti-levelinvertersusingharmonysearch algorithm," 2008 IEEE 2nd International Powerand Energy Conference, Johor Bahru, 2008, pp. 646-650,doi:10.1109/PECON.2008.4762555.
- [9] N.SahuandN.D.Londhe, "Selectiveharmoniceliminationinfivelevelinverterusingsinecosinealgorithm," 2017 IEEE International Conference on Power, Control, Signals and Instrumentation Engineering (ICPCSI), Chennai, 2017, pp. 385-388, doi:10.1109/ICPCSI.2017.8392322
- [10] S. Bhalerao, S. Pawar and N. Patwardhan, "Designand Implementation of Bipolar SHE-PWM Single PhaseInverterforLowerOrderHarmonic-Suppression,"20183rdIEEEInternationalConferenceonRecentTrends in Electronics, Information & CommunicationTechnology(RTEICT),Bangalore,India,2018,pp.1066-1071, doi:
- 10.1109/RTEICT42901.2018.9012349
- [11] P.MamathaandC. Venkatesh, "PerformanceImproved Multilevel Inverter with Selective HarmonicElimination," 2017 International Conference on RecentTrendsinElectrical, Electronics and ComputingTechnologies (ICRTEECT), Waran-

- gal,2017,pp.133-138,doi:10.1109/ICRTEECT.2017.24.
- [12] K.Ganesan, K.Barathi, P.Chandrasekar, D.Balaji"Selective Harmonic Elimination of Cascaded MultilevelInverterUsingBATAlgorithm"Volume21, 2015
- [13] A.Kavousi, B. Vahidi, R. Salehi, M.K. Bakhshizadeh, N. Farokhnia and S. H. Fathi, "Application of the BeeAlgorithm for Selective Harmonic Elimination StrategyinMultilevelInverters," in IEEE Transactions on Power Electronics, vol. 27, no. 4, pp. 1689-1696, April 2012, doi:10.1109/TPEL.2011.2166124.
- [14] H.Zhao, S. Wangand A. Moeini, "Critical Parameter Design for a Cascaded H-Bridge With Selective Harmonic Elimination/Compensation Based on Harmonic Envelope Analysis for Single-Phase Systems, "in IEEE Transactions on Industrial Electronics, vol. 66, no. 4, pp. 2914-2925, April 2019, doi:10.1109/TIE.2018.2842759.
- [15] M. T. Yaqoob, "Selective Harmonic Elimination inCascaded H-Bridge Multilevel Inverters using ParticleSwarmOptimization:Areview,"201913thIn-

- ternationalConferenceonMathematics,Actuarial-Science,ComputerScienceandStatistics(MACS),-Karachi,Pakistan,2019,pp.1-5,doi:10.1109/MACS48846.2019.9024783
- [16] S.D.PatilandS.G.Kadwane,"Applicationo-foptimizationtechniqueinSHEcontrolledmultilevelinverter,"2017InternationalConferenceonEnergy,Communication,DataAnalyticsandSoftComputing(ICECDS),Chennai,2017,pp.26-30,doi:10.1109/ICECDS.2017.8390050
- [17] P. Mishra and A. Mahesh, "Sine cosine algorithmbasedstaircasemodulationforcascadedH-bridge-inverter,"20193rdInternationalconferenceonElectronics,CommunicationandAerospaceTechnology (ICECA), Coimbatore, India, 2019, pp. 914-919,doi:10.1109/ICECA.2019.8822115.
- [18] Sahu, N., & Londhe, N.D" Optimization based-selective harmonic elimination in multi-level inverters", 2017 National Power Electronics Conference (N-PEC).

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